

1 / 5

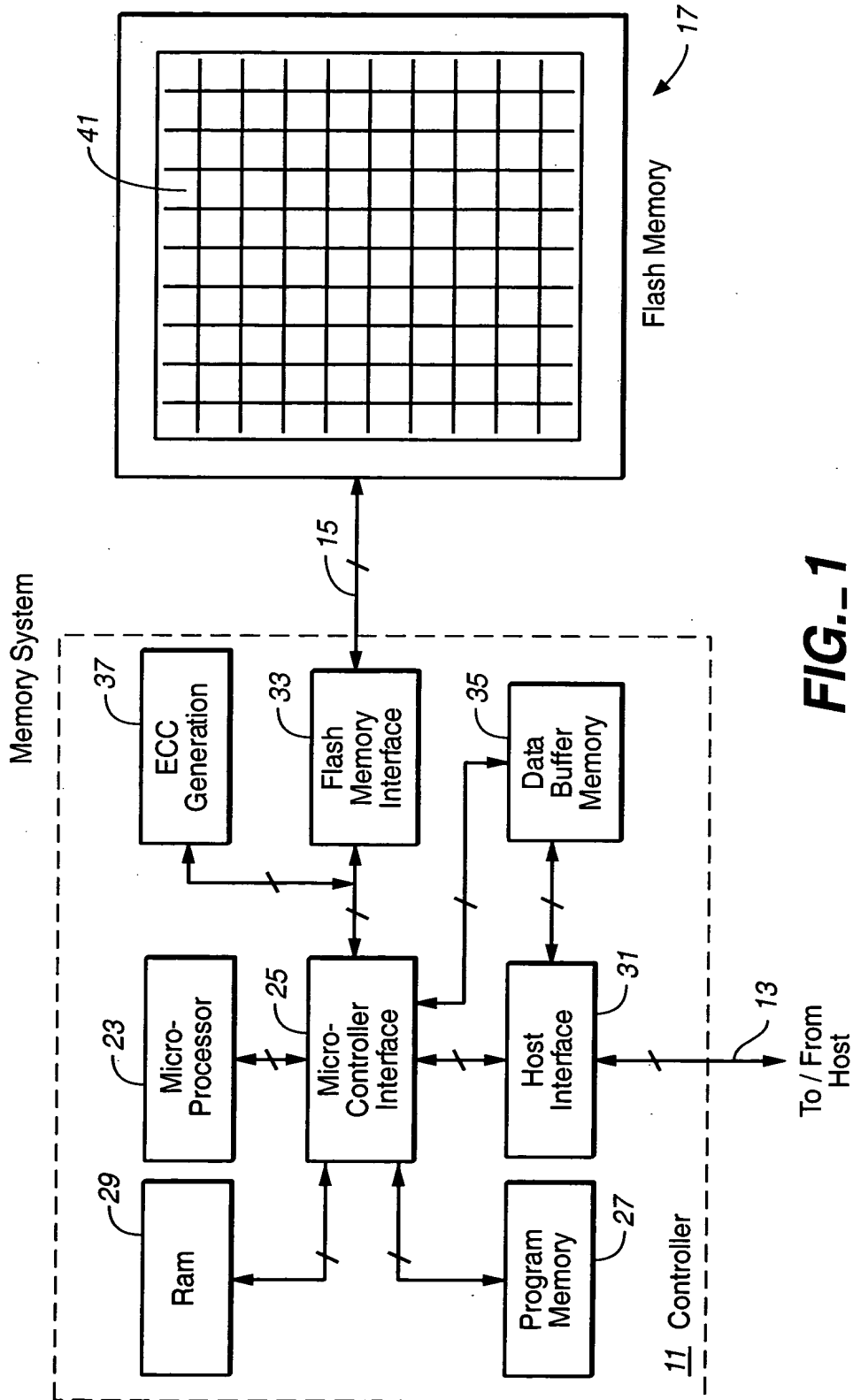


FIG. 1

2 / 5

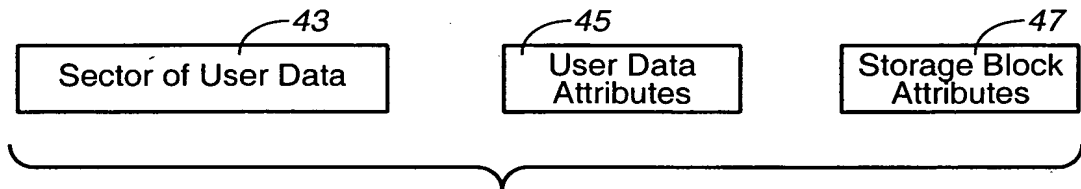


FIG. 2
(PRIOR ART)

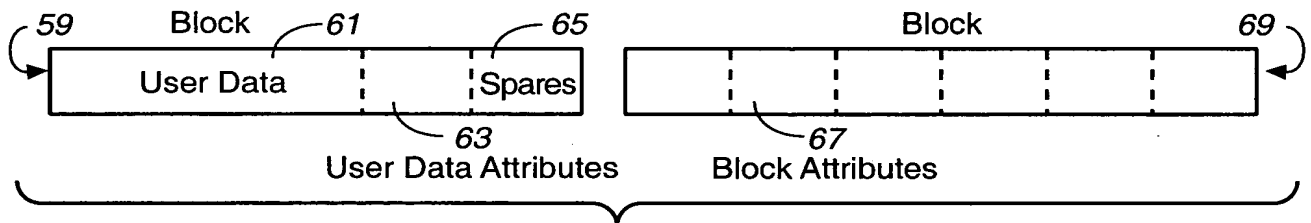
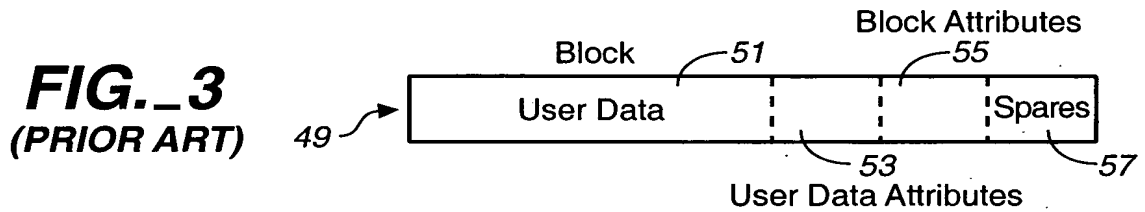


FIG. 4
(PRIOR ART)

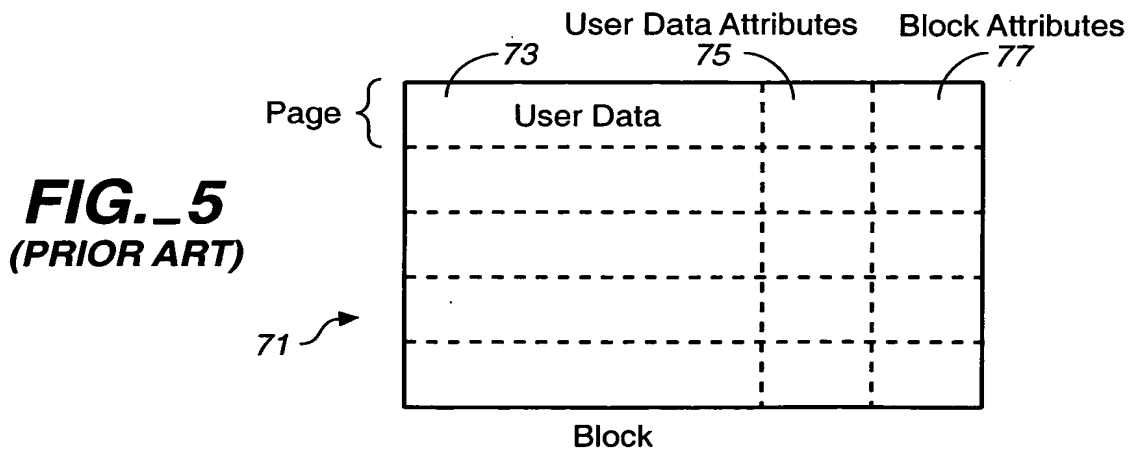


FIG. 5
(PRIOR ART)

FIG._6A

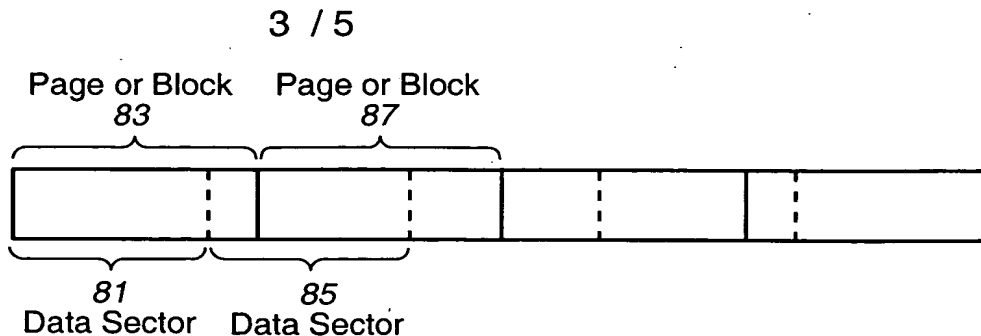


FIG._6B

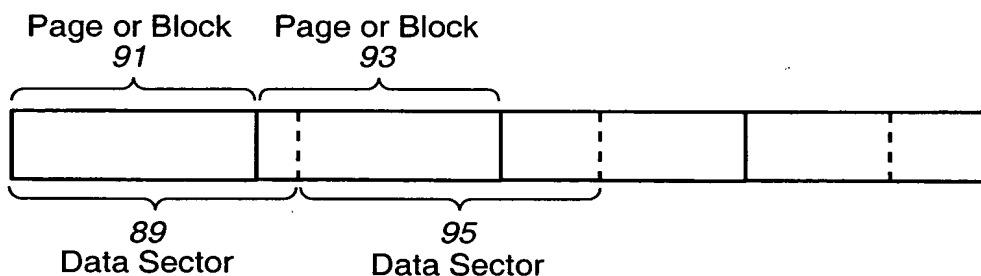


FIG._8

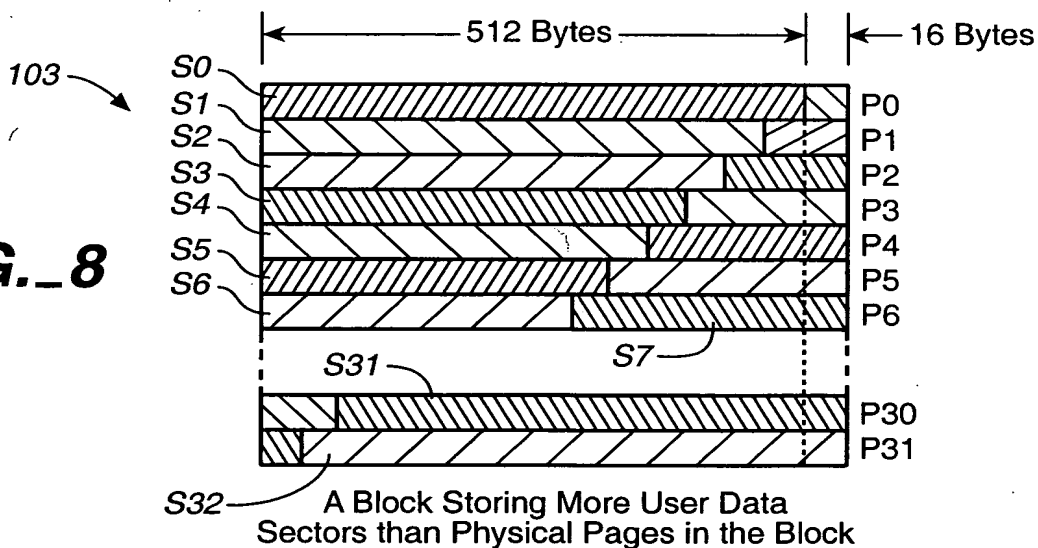
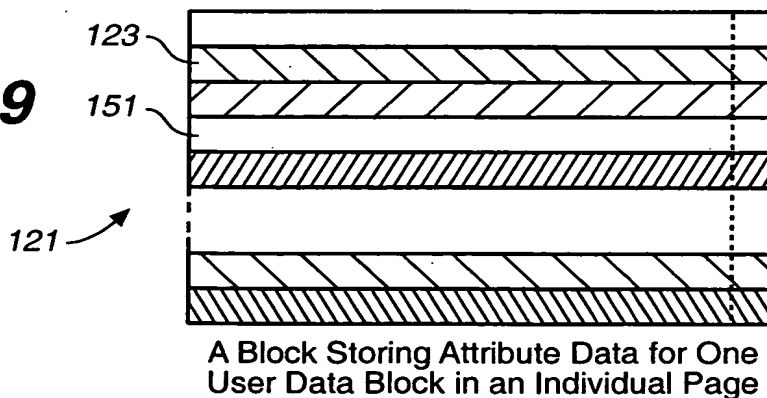
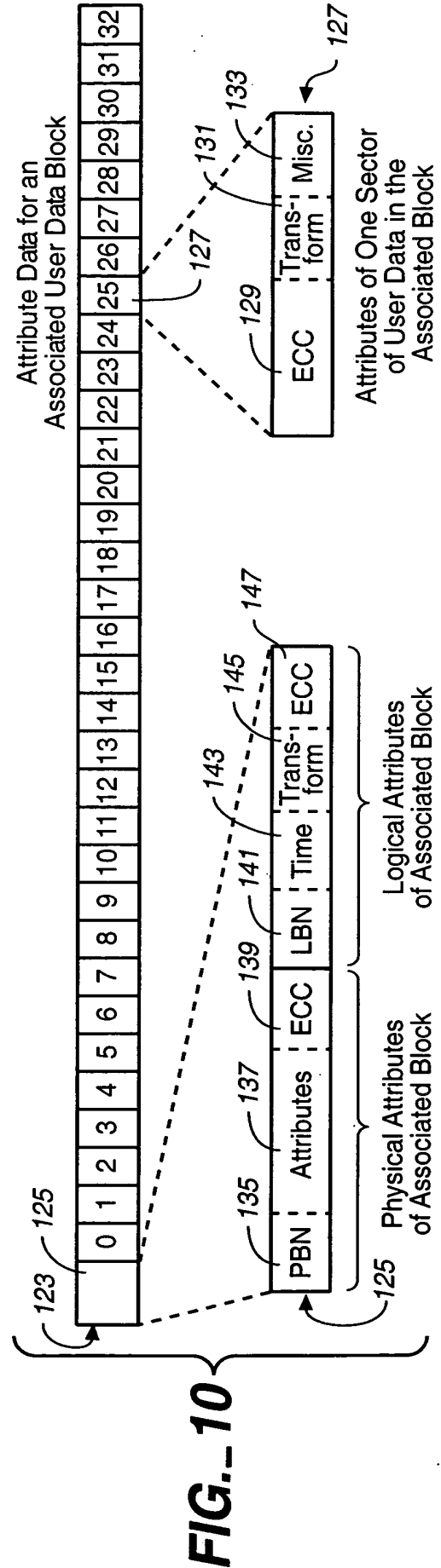


FIG._9





5 / 5

Table in Controller RAM Built From
 Fig. 10 Attribute Records

FIG. 11

Address (LBN)	User Data Block	Attribute Data Page	
	PBN	PBN	Page No.
25			
26			
27			
28			

155 → 153 → 157 → 159 →

FIG. 12
 (PRIOR ART)

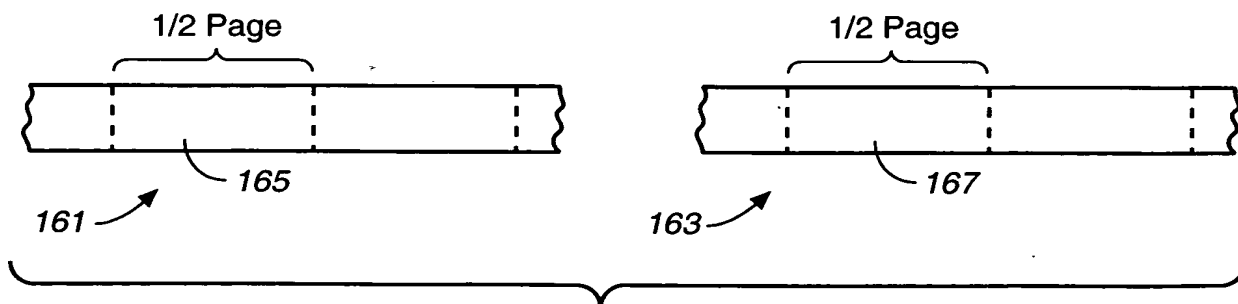
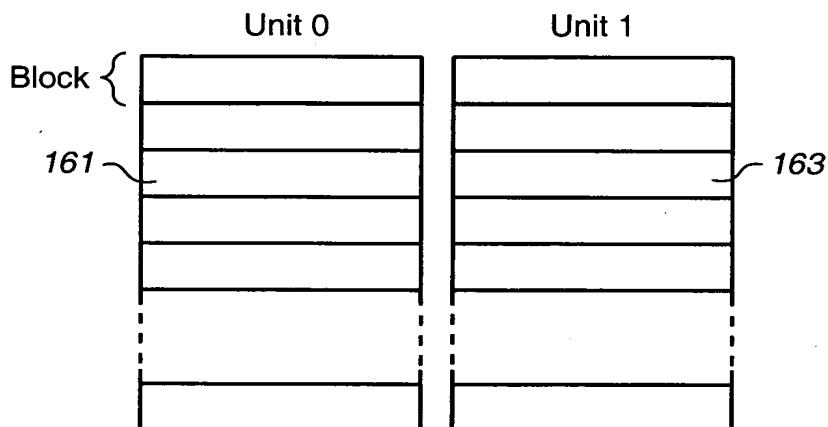


FIG. 13